

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FIRST NAMED INVENTOR : Junichi KISHIDA **Confirmation No.: 5285**
FOR : BOOTABLE NAND FLASH MEMORY ARCHITECTURE
APPLICATION NO. : 10/716,159
FILING DATE : November 18, 2003
EXAMINER : Daniel B. Ko
ART UNIT : 2189
CUSTOMER NO. : 23380

AMENDMENT

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office action dated June 23, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on **page 2** of this paper.

Remarks/Arguments begin on **page 7** of this paper.